Runtime and Quality Tradeoffs in FPGA Placement and Routing:

* I also wanted to hear about cases where quality is critical, for instance operating systems/ medical usage.
* What happens if we try different routing algorithms? Doesn’t it really depends on the algorithm implementations?
* In the routing algorithm, does the quality even matters? We can find a path for instance after 4 seconds with 100% accuracy if we just hit the correct paths after multiple errors

PathFinder: A Negotiation-Based Performance-Driven Router for FPGAs:

* What does signal routing means?
* What happens then it’s an undirected graph?
* What happens when there’s multiple penalties/policies?

FlowMap: An Optimal Technology Mapping Algorithm for Delay Optimization in Lookup-Table Based FPGA Designs:

* What is the use of those LUTs?
* If we decide on different sizes of K how it affects? Very big K or very small one?